

Appl. No. 10/734,834  
Amdt. Dated October 6, 2006  
Reply to Office Action of June 7, 2006

Attorney Docket No. 81876.0059  
Customer No.: 26021

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1 – 21 (Cancelled)

22. (Previously presented) A driver for driving a load with a secondary power supply voltage obtained by stepping a primary supply voltage level using a charge pump circuit that has a multiplicity of stages, each of said stages including a switching element and a capacitor, wherein:

an associated capacitor of the first stage of said charge pump circuit is energized by the one of the voltages impressed on the capacitors of said multiplicity of stages that is closest in value to the primary supply voltage level; and

said associated capacitor has a voltage dependent capacitance under a given applied voltage and has a different structure, a low resistance and a large area in comparison with at least one of the other said capacitors of the subsequent stages.

23. (Previously presented) A driver for driving a load by a secondary power supply voltage obtained by stepping a primary power supply voltage using a charge pump circuit that has a multiplicity of stages each including a switching element and a capacitor, wherein

a first capacitor of a first stage (first stage capacitor) of said charge pump circuit is energized by the lowest voltage of the voltages impressed on the capacitors of said multiplicity of stages; and

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said first-stage capacitor has a voltage dependent capacitance under a given voltage and has a different structure, a low resistance and a large area in comparison with at least one of said capacitors of the subsequent stages.

24. (Previously presented) The driver of Claims 22 or 23, wherein said primary supply voltage level comprises the primary power supply voltage level.

25. (Previously presented) The driver of Claim 23, wherein the voltage to which the first capacitor is energized is the lowest voltage of the voltage impressed on the capacitor of said multiplicity of stages.

26. (Previously presented) The driver of Claims 22 or 23, wherein the primary supply voltage is stepped-up to provide the secondary power supply voltage and each of the stages is a stepped up stage.

27. (Cancelled)

28. (Previously presented) The driver of Claim 22, wherein said associated capacitor has a lower internal resistance than of at least the one of said capacitors of the subsequent stages.

29. (Previously presented) The driver of Claim 23, wherein said first stage capacitor has a lower internal resistance than of at least the one of said capacitors of the subsequent stages.

30. (Previously presented) The driver of Claim 28, wherein the associated capacitor has a lower internal resistance than all of the other of the capacitors of the subsequent stages.

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31. (Previously presented) The driver of Claim 29, wherein the first capacitor has a lower internal resistance than all of the other of the capacitors of the subsequent stages.